



Institution Making Students Ready for "Industry 4.0"

Editor in Chief

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Asso. Prof / EEE

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*Editorial
Board*



INDEX

- ✚ Editorial Board
- ✚ Vision and Mission
- ✚ Major Event
- ✚ Students Achievements
- ✚ Faculties Achievements



- To provide students with high quality education so that they are well prepared to become high caliber Electrical and Electronics Engineers, and it aspires to grow to the level of gaining global recognition.



- To develop competent technocrats who strive continuously in pursuit of professional excellence in the field of Electrical and Electronics Engineering.
- To develop and sustain a culture of research while promoting values, ethics and professionalism.
- To offer well balanced curriculum to help students acquire professional competencies and to arrange placements for students.
- To develop state of art infrastructure and research for effective teaching learning process.
- Strengthening of soft skills especially for rural students through co-curricular and extra-curricular activities.

MAJOR EVENT

Our college receive GRADE "A" in the Accreditation by NAAC



JJCET-IIC organized to field visit at NIT, Trichy (CEDI incubation center and Third eye super computer lab) on 18.07.2024. our Third year Students V.Prince David, S.Santhose, M.Krishnamoorthy, S.Durgadevi and the faculty J.Auml Arokiaraj, AP are participated the visit.



STUDENTS ACHIEVEMENTS

CDC of JJCET organized campus drive on 04.07.2024. Final year student D.Jenita placed in US based company EMERSON.

J.J.COLLEGE OF ENGINEERING AND TECHNOLOGY
Approved by AICTE | Affiliated to Anna University | ISO 9001 : 2015 Certified | 3807
SOWDAMBIKAA GROUP OF INSTITUTIONS

CDC Career Development Center

EXCITING OPPORTUNITY TO GET DIRECT ONROLL JOB @ US BASED COMPANY

POOLED CAMPUS DRIVE

EMERSON

ELIGIBILITY :

1. B.E/ B.Tech (2024 passed out with 75% and above / No history of Arrears.)
2. Department- All Branches (Except CSE, IT, Civil and Aero)

NOTE: Female Candidates are highly appreciated. Company will provide immediate Placement.

Salary: 5 LPA

Designation:

- Trainee (1st year)
- Engineer (2nd Year onwards)

04/07/2024 9:30 A.M.


Auditorium, JJCET, Trichy



FACULTIES ACHIEVEMENTS

Dr.K.Jayakumar, HoD & Dean (Electrical Science) has published paper from the JCI journal.

Journal of Cybersecurity and Information Management (JCIIM) Vol. 14, No. 02, PP. 287-299, 2024



Development of a Cryptographic Model Using Digits Classification for Cyber Security Applications

Dr. K. Jayakumar^{1*}, Mrs. K. Sivakami², P. Logamurthy³, Dr.P.Sathiyamurthi⁴, N. Chandrasekaran⁵

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Mrs P.Rathidevi AP/EEE has participated FDP on “IOT and Semiconductors-Synergies and Future” organized by KCG college of Engineering, Chennai from 08.07.24 to 12.07.24.



The certificate features logos for IEEE KCG Chapter, KCG College of Technology, and accreditation bodies A+, NA, and nif. The text certifies Mrs. RATHI DEVI P, EEE from J.J. College of Engineering and Technology for participating in a five-day FDP on IoT and Semiconductors - Synergies and Future from July 8th to 12th, 2024. It is signed by Dr. Kavitha Balamurugan (Convener), Dr. T. Anuradha (Convener), and Dr. M Muthukannan (Principal).

CERTIFICATE OF PARTICIPATION

This is to certify that

Mrs.RATHI DEVI P, EEE
J.J. COLLEGE OF ENGINEERING AND TECHNOLOGY

has participated in A Five Day Faculty Development Program on **IoT and Semiconductors - Synergies and Future** organized by **Department of Electronics & Communication Engineering and Department of Electrical & Electronics Engineering** from 8th to 12th July 2024.

Dr. Kavitha Balamurugan
Convener

Dr. T. Anuradha
Convener

Dr. M Muthukannan
Principal