



# J.J. COLLEGE OF ENGINEERING AND TECHNOLOGY

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**SOWDAMBIKAA GROUP OF INSTITUTIONS**



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*Institution Making Students Ready for “Industry 4.0”*

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- To provide students with high quality education so that they are well prepared to become high caliber Electrical and Electronics Engineers, and it aspires to grow to the level of gaining global recognition.



- To develop competent technocrats who strive continuously in pursuit of professional excellence in the field of Electrical and Electronics Engineering.
- To develop and sustain a culture of research while promoting values, ethics and professionalism.
- To offer well balanced curriculum to help students acquire professional competencies and to arrange placements for students.
- To develop state of art infrastructure and research for effective teaching learning process.
- Strengthening of soft skills especially for rural students through co-curricular and extra-curricular activities.

## Major Events

Our college celebrated the 77th Republic Day with great enthusiasm. The Principal hoisted the National Flag, delivered the Republic Day address, and distributed prizes to the winners of various events. The celebration concluded with the distribution of sweets.



# FACULTIES' ACHIEVEMENTS

"Mr. P. John Britto published a research article titled 'A Nine-Level Quadratic Boost Common Ground Inverter Topology with Reduced Voltage Stress on Switches' in the *Scientia Iranica* journal."  
(Doi- [10.24200/SCI.2025.66257.994](https://doi.org/10.24200/SCI.2025.66257.994)!)

The screenshot displays the journal's homepage for the article. At the top, the journal title "SCIENTIA IRANICA" is prominently featured in a serif font, with the subtitle "International Journal of Science & Technology" below it. A navigation menu includes links for Home, Browse, Journal Info, Guide for Authors, Submit Manuscript, Reviewers, Recent Published Articles, Contact Us, Login, and Register. The article title is displayed in a bold, black font. Below the title, it is noted as an "Articles in Press" document type. The authors listed are John Britto Pitchai and Vijayarajan Periyasamy. Their affiliations are provided: Department of Electrical and Electronics Engineering, J.J. College of Engineering and Technology, Tiruchirappalli, India, and Department of Electrical and Electronics Engineering, University College of Engineering BIT Campus, Tiruchirappalli, India. A DOI link is provided: [10.24200/SCI.2025.66257.994](https://doi.org/10.24200/SCI.2025.66257.994). The abstract text describes the article's focus on transformerless multilevel inverters (TMLIs) for grid-connected solar photovoltaic (PV) systems, highlighting a proposed nine-level topology with three switched capacitors (SCs) to reduce leakage current and voltage stress. On the right side, a sidebar contains a thumbnail of the article cover, a status box indicating it is an "Articles in Press, Accepted Manuscript" available from October 22, 2025, and a "Files" section with links to XML and PDF (2.82 M). Below this are buttons for "History", "Share", "How to cite", and "Statistics".

